

# Vlsi Lab External Viva Question And Answer

## Chapter 1 : Vlsi Lab External Viva Question And Answer

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Arm microcontroller lab manual dept of ece, hmsit, tumkur 1 contents 1. introduction 2 viva question 73 . arm microcontroller lab manual dept of ece, hmsit, tumkur 2 introduction acorn computer group, and vlsi technology. in 1991, arm introduced the arm6 processor family, and vlsi became the initial licensee. subsequently, additional The lab activities will generally be one week labs. however there will be some longer labs toward the end that will be two week labs. before the lab, the student should read through the lab description and perform the pre-lab exercises. generally, the prelab - exercises are the “hand” design for the circuit being studied.Very large scale integration (vlsi) design, embedded systems (es) and based 14eve16 vlsi design and embedded system lab -1. -. 3 michael keating, pierre bricaud, “reuse methodology manual for m.tech 13 batch who are eligible to submit the main project lab external 26.3.2015 afternoon embedded real time operating systems.Questions all carrying equal marks. duration of each external examination will be three hours. similarly, the external evaluation for all lab courses (50% of the total marks) will be based on the evaluation/viva-voce conducted by an external examiner (from the relevant field) nominated/approved by the competent authority.Questions all carrying equal marks. duration of each external examination will be three hours. similarly, the external evaluation for all lab courses (50% of the total marks) will be based on the evaluation/viva-voce conducted by an external examiner (from the relevant field) nominated/approved by the competent authority.B. external examiner shall carry out the evaluation for 100 marks. c e average of marks allotted by the internal and external examiner shall be the final marks of the project evaluation. d. viva-voce examination of project work shall be conducted jointly by internal and external examiner for 100 marks.

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